

revised 1/29/2008		
<b>ADVANCED DESIGN SYSTEM 2008, RFDE &amp; GoldenGate CODEWORD TABLE</b>		
(codeword / license delivered with supported ADS products and new purchase of ADS, RFDE, & GG perpetual and TBL products)		
<b>October 8, 2007</b>		
P/N	P/N DESCRIPTION	CODEWORD NAME
E4687L/TBL	Broadband SPICE Model Generator	mom_spice_broadband
E5610L/TBL	Passive Circuit DesignGuide	dg_passives
E5611L/TBL	Power Amplifier DesignGuide	dg_pwr_amp
E5612L/TBL	Oscillator DesignGuide	dg_oscillator
E5613L/TBL	Phase-Locked Loop DesignGuide	dg_pll
E5614L/TBL	Linearizer DesignGuide	dg_linear
E5615L/TBL	Mixer DesignGuide	dg_mixer
E5616L/TBL	Bluetooth DesignGuide	dg_multi_carrier
E5617L/TBL	RF System DesignGuide	dg_commsys
E5618L/TBL	Filter DesignGuide	dg_filter
E5623L/TBL	Design Guide Bundle	pb_design_guides
E5720L	Connection Manager	link_connect_mgr, link_measampmodeling
E5771TBL	RFIC Limited Term Package	ltp_rfic_dg
E5772TBL	RF and Microwave Limited Term Package	ltp_rf_mw_dg
E5773TBL	CommSys Limited Term Package	ltp_comm_dsp_dg
E5774TBL	Integrated Design Limited Term Package	ltp_intergrated_dg
E5776TBL	MIMIC Limited Term Package	ltp_mmic
E5777TBL	Analog Designer Time Based License	ltp_analog
E5778TBL	RF & Microwave Entry Level Time Based License	ltp_rfmw_entry
E5779TBL	DesignGuide Bundle Limited Term Package	ltp_design_guides
E8811L/TBL	MMIC Designer Pro	ads_datadisplay, ads_layout, ads_schematic, mom_opt, mom_vis, momentum, mom_obj, sim_harmonic, sim_linear, trans_gdsii
E8812L/TBL	MMIC Designer Premier	ads_datadisplay, ads_drc, ads_layout, ads_schematic, dg_passives, mom_adv_composer, mom_opt, mom_vis, momentum, mom_obj, sim_harmonic, sim_linear, sim_statistics, trans_dxf_flat, trans_dxf_hier, trans_gdsii, trans_gerber
E8819L/TBL	EMDS for ADS Suite	emds_environment, emds_3d_engine_a, emds_for_ads
E8822L/TBL	Agilent Ptolemy Fixed Point Analysis	sim_fixedpoint
E8823L/TBL	Agilent Ptolemy Simulator	sim_systeme
E8824L/TBL	Statistical Design	sim_statistics
E8825L/TBL	Digital Filter	sim_dfilter
E8827L/TBL	Advanced Comms Models	mdl_adv_comm
E8828L/TBL	Signal Integrity Verification Toolkit	ads_si_verification
E8829L	Comms Verification Bundle	b_comms_verification
E8850L/TBL	Communication System Designer (node locked only)	ads_datadisplay, ads_lite, mdl_systemlib, sim_syslinearlite
E8851L/TBL	Communications System Designer Pro	ads_datadisplay, ads_schematic, mdl_adv_comm, mdl_systemlib, sim_dfilter, sim_fixedpoint, sim_statistics, sim_syslinear, sim_systime, sim_usermodels

P/N	P/N DESCRIPTION	CODEWORD NAME
E8852L/TBL	Communications System Designer Premier	ads_datadisplay, ads_schematic, mdl_ant_array, mdl_ant_cdma, mdl_ant_gsm, mdl_ant_wcdma, mdl_adv_comm, mdl_antenna, mdl_propagation, mdl_systemlib, sim_dfilter, sim_fixedpoint, sim_hdl_cosim, sim_statistics, sim_syslinear, sim_systime, sim_usermodels
E8853L/TBL	RF System Simulator	sim_syslinear
E8854L/TBL	RF Systems Models	mdl_systemlib
E8856L/TBL	Antenna & Propagation Models	mdl_ant_array, mdl_ant_cdma, mdl_ant_gsm, mdl_ant_wcdma, mdl_antenna, mdl_propagation
E8857L	CDMA Design Library	mdl_ant_cdma, mdl_antenna, mdl_cdma
E8859L	GSM Design Library	mdl_ant_gsm, mdl_antenna, mdl_gsm
E8866L/TBL	HDL co-sim for Agilent Ptolemy	sim_hdl_cosim
E8868L	DTV Design Library	mdl_dtvtrans
E8869L/TBL	Mobile WiMax Wireless Library	mdl_wimax_mobile, mdl_adv_comm
E8870L/TBL	Fixed WiMax Wireless Library	mdl_wimax_fixed, mdl_adv_comm
E8871L/TBL	802.11n Wireless Library	mdl_802_11_n, mdl_adv_comm
E8873L	TD-SCDMA Design Library	mdl_antenna, mdl_propagation, mdl_tdscdma
E8874L	WLAN Design Library	mdl_antenna, mdl_wlan, mdl_propagation
E8875L	3GPP W-CDMA Design Library	mdl_ant_array, mdl_ant_wcdma, mdl_antenna, mdl_propagation, mdl_wcdma3g
E8877L	CDMA2000-compliant Design Library	mdl_ant_cdma, mdl_antenna, mdl_cdma2k
E8878L	CDMA1xEV Design Library	mdl_1xev
E8879L	EDGE Design Library	mdl_ant_gsm, mdl_antenna, mdl_edge
E8881L/TBL	Linear Simulator	sim_linear
E8882L/TBL	Harmonic Balance Simulator	sim_harmonic
E8883L/TBL	Circuit Envelope Simulator	sim_envelope
E8884L/TBL	High Frequency Spice	sim_transient
E8885L/TBL	Convolution Simulator	sim_convolution
E8886L/TBL	Verilog-A Compiler	sim_veriloga
E8887L/TBL	HSUPA Design Library	mdl_hsupa, mdl_wcdma3g, mdl_adv_comm, mdl_ant_array, mdl_ant_wcdma, mdl_antenna, mdl_propagation
E8888L/TBL	RFIC Designer Pro	ads_datadisplay, ads_schematic, sim_envelope, sim_harmonic, sim_linear, sim_transient
E8889L/TBL	RFIC Designer Premier	ads_datadisplay, ads_schematic, mdl_systemlib, sim_convolution, sim_envelope, sim_harmonic, sim_linear, sim_statistics, sim_transient, sim_usermodels
E8890L/TBL	Analog Model Development Kit	sim_usermodels
E8894L/TBL	RFIP Encoder	ads_encoder
E8895L/TBL	3GPP LTE Wireless Library	mdl_3gpp_lte
E8896L/TBL	Wireless Networking Verification Bundle	b_mdl_wireless_network
E8897L/TBL	2G/3G Cellular Verification Bundle	b_mdl_cellular
E8898L/TBL	Mature Wireless Verification Bundle	b_mdl_mature
E8899L/TBL	Integrated Wireless Verification Library	b_mdl_wireless_integrated
E8900L/TBL	Design Environment	ads_schematic
E8901L/TBL	Data Display	ads_datadisplay
E8902L/TBL	Layout	ads_layout
E8903L/TBL	IGES Translator	trans_iges
E8904L/TBL	GDSII Translator	trans_gdsii
E8905L/TBL	DXF Translator	trans_dxf_hier
E8906L/TBL	Gerber & DXF Translator	trans_dxf_flat, trans_gerber

P/N	P/N DESCRIPTION	CODEWORD NAME
E8907L/TBL	ADS Layout Design Rule Checker	ads_drc
E8909L/TBL	Layout Translator Bundle	trans_dxf_flat, trans_dxf_hier, trans_gdsii, trans_gerber, trans_iges
E8910L/TBL	Microwave Circuit Designer (node locked only)	ads_datadisplay, ads_schematic, sim_harmonic, sim_linear
E8911L/TBL	Microwave Circuit Designer Pro	ads_datadisplay, ads_layout, ads_schematic, sim_harmonic, sim_linear, sim_statistics
E8912L/TBL	Microwave Circuit Designer Premier	ads_datadisplay, ads_layout, ads_schematic, mdl_systemlib, sim_envelope, sim_harmonic, sim_linear, sim_statistics, sim_usermodels
E8914L/TBL	Physical Designer Pro	ads_drc, ads_layout, trans_dxf_flat, trans_dxf_hier, trans_gdsii, trans_gerber
E8915L/TBL	Physical Designer Premier	ads_drc, ads_layout, mom_opt, mom_vis, momentum, mom_obj, trans_dxf_flat, trans_dxf_hier, trans_gdsii, trans_gerber
E8917L/TBL	Gerber File Importer	trans_gerber_union
E8919L/TBL	Momentum Circuit Designer	ads_datadisplay, ads_layout, ads_schematic, momentum, mom_obj, sim_linear
E8920L/TBL	Momentum EM Bundle	momentum, mom_obj, mom_opt, mom_vis
E8921L/TBL	Momentum Planar EM Simulator	momentum, mom_obj
E8922L/TBL	Momentum Visualization	mom_vis
E8925L/TBL	Momentum Optimization	mom_opt
E8926L/TBL	Advanced Model Composer	mom_adv_composer
E8933L/TBL	WiMedia Wireless Library	mdl_wimedia, mdl_antenna, mdl_propagation, mdl_adv_comm
E8937L/TBL	Verilog-AMS Compiler	sim_verilog_ams
E8940L	RF Designer (node locked only)	ads_datadisplay, ads_lite, sim_linearlite
E8942L/TBL	RF Board Designer Pro	ads_datadisplay, ads_layout, ads_schematic, mdl_rfelements, sim_harmonic, sim_linear, sim_statistics
E8943L/TBL	RF Board Designer Premier	ads_datadisplay, ads_layout, ads_schematic, mdl_multilayer, mdl_rfelements, mdl_systemlib, sim_envelope, sim_harmonic, sim_linear, sim_statistics
E8949L/TBL	IBIS Model Library	mdl_ibis
E8950L/TBL	RF Passive Circuit Models	mdl_rfelements
E8951L/TBL	Multilayer Interconnect Models	mdl_multilayer
E8962L/TBL	Legacy DXF Heirarchical Translator	trans_dxf_hier_legacy
E8963L/TBL	Legacy Gerber and DXF Flattened Translator	trans_dxf_flat_legacy, trans_gerber_legacy
E8970L/TBL	RFIC Dynamic Link for Cadence	trans_idf
E8971L/TBL	ADS Bundle License - Linear	pb_ak1
E8972L/TBL	ADS Bundle License - Linear-Layout	pb_ak2
E8973L/TBL	ADS Bundle License - Linear-EM-Layout	pb_ak3
E8974L/TBL	ADS Bundle License - Linear-Non Linear	pb_ak4
E8975L/TBL	ADS Bundle License - Linear-Non Linear-Layout	pb_ak5
E8976L/TBL	ADS Bundle License - Linear-Non Linear-EM-Layout	pb_ak6
E8977L/TBL	ADS Bundle License - Linear-Non Linear-SPICE	pb_ak7
E8982L/TBL	SiP Module Designer	b_sipmodule_designer

P/N	P/N DESCRIPTION	CODEWORD NAME
E8983L/TBL	SiP Module Designer Pro	b_sipmodule_designer_pro
E8984L/TBL	SiP Module Designer Premier	b_sipmodule_designer_premier
E8986L/TBL	High Speed Analog Designer	ads_schematic, ads_datadisplay, sim_linear, sim_transient, dg_passives, mdl_ibis, mom_spice_broadband
E8987L/TBL	High Speed Analog Designer Pro	ads_schematic, ads_datadisplay, ads_layout, ads_si_verification, sim_linear, sim_transient, sim_convolution, sim_statistics, mdl_rfelements, mdl_ibis, mdl_multilayer, momentum, mom_obj, mom_spice_broadband, mom_vis, trans_dxf_flat, trans_dxf_hier, trans_gdsii, trans_gerber, trans_iges, dg_passives
E8988L/TBL	High Speed Analog Designer Premier	ads_schematic, ads_datadisplay, ads_layout, ads_si_verification, sim_linear, sim_transient, sim_convolution, sim_harmonic, sim_statistics, sim_systeme, sim_usermodels, mdl_rfelements, momentum, mom_obj, mom_vis, mom_opt, mom_spice_broadband, trans_dxf_flat, trans_dxf_hier, trans_gdsii, trans_gerber, trans_iges, mdl_multilayer, mdl_systemlib, mdl_ibis, dg_passives, dg_filter
E9010L/TBL	ADS Signal Integrity Designer	pb_si_designer
E9011L/TBL**	ADS Signal Integrity Designer Pro	pb_si_designer_pro
E9012L/TBL**	ADS Signal Integrity Designer Premier	pb_si_designer_3, emds_environment, emds_3d_engine_a
W1105L/TBL	GoldenGate/RFDE Momentum Integration	rfde_environment, rfde_momentum_int, ads_datadisplay
W1106L/TBL	GoldenGate/RFDE Momentum Suite	rfde_environment, rfde_momentum_int, ads_datadisplay, momentum, mom_obj, mom_vis
W1107L/TBL	RFDE Wireless Test Bench	rfde_wtb_int
W1112L/W1112TBL	RFIC Combo - GoldenGate/RFDE/ADS	ltp_rfic_rfde_combo_gg, gpp, qwave, skilld, va2gg, ads_datadisplay, rfde_environment, burstdsim (qty 4)
W1120L/TBL	Ptolemy AMSD Integration	rfde_amsd_int, rfde_environment
W1121L/TBL	Ptolemy AMSD Suite	rfde_amsd_int, rfde_environment, sim_systeme, ads_datadisplay
W1421L/TBL	Genesys Synthesis for ADS	ads_circuit_synthesis
W1422L/TBL	RF Architect for ADS	ads_rf_architect
W2001L/TBL	GoldenGate Simulator	ggsim, gpp, qwave, skilld, va2gg, ads_datadisplay, rfde_environment, burstdsim (qty 4)
W2002L/TBL	GoldenGate Superlicense 10x time-based license	ggsims10, burstdsim (qty 10)
W2003L/TBL	GoldenGate Superlicense 20x time-based license	ggsims20, burstdsim (qty 20)
W2005L/TBL	PLL Simulator for Golden Gate	ggpll
W2010L/TBL	GoldenGate Core	gpp, qwave, va2gg, ggsimcore, skilldcore
W2011L/TBL	GoldenGate RFIC Designer	rfde_premier_gg, gpp, qwave, skilld, va2gg, burstdsim (qty 4), rfde_environment, ads_datadisplay
W2013L/TBL	GoldenGate Enterprise	ltp_intdesrfde_combo_gg, gpp, qwave, skilld, va2gg, ads_datadisplay, rfde_environment, burstdsim (qty 4)
4/10/2007	W2001: rfde_environment, ads_datadisplay codewords added	
10/1/2007	W2002L, W2003L, W2010L/TBL, W2011L/TBL, and W2013L/TBL new products on sale eff 10/1/07.	

P/N	P/N DESCRIPTION	CODEWORD NAME
10/3/2007	**Effective with the ADS 2008 release, E9011L/TBL and E9012L/TBL will no longer contain E8917L, Gerber Union.	
10/8/2007	E8982L/TBL, E8983L/TBL, and E8984L/TBL on sale effective 11/1/07.	
10/23/2007	Effective 11/1/07 codeword changed for W2011, W1112, and W2013 - ggsim is removed and gg has been added to each bundle name. Xplx has also been removed.	
10/30/2007	Effective 11/1/07 - TBL option set up for most products that didn't previously offer TBL.	
11/12/2007	Added E8962L/TBL, E8963L/TBL. These products will ship with ADS 2008.	
11/12/2007	Added W2005L/TBL..	
1/3/2008	Added burstsim to W2001, W2013, W2011, and W1112	
1/29/2008	Removed xplx from W2001L/TBL and W2010L/TBL. This feature is no longer needed.	